



EXPERT DISCUSSION: HOW WILL AI APPLICATIONS AFFECT HIGH SPEED LINK DESIGN?

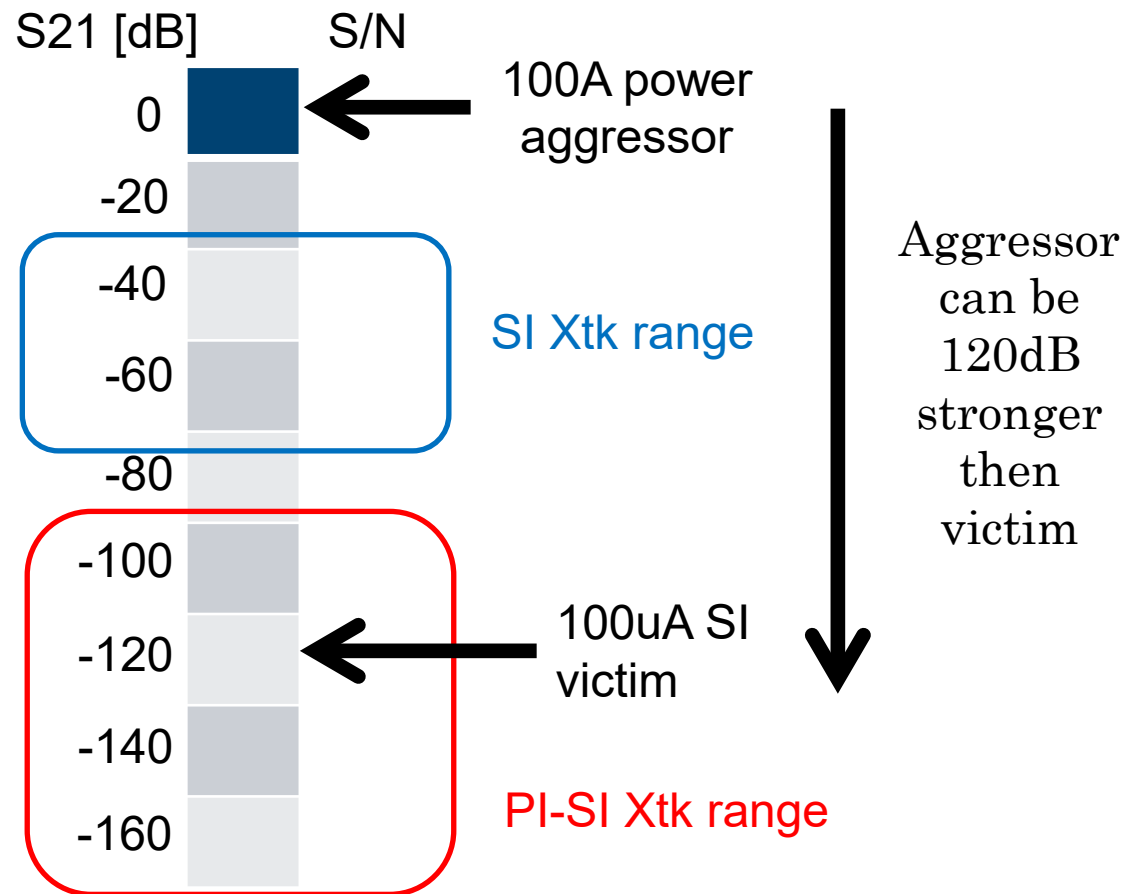
DesignCon 2025 panel

Istvan Novak, Samtec
Presented by Steve Sandler, Picotest

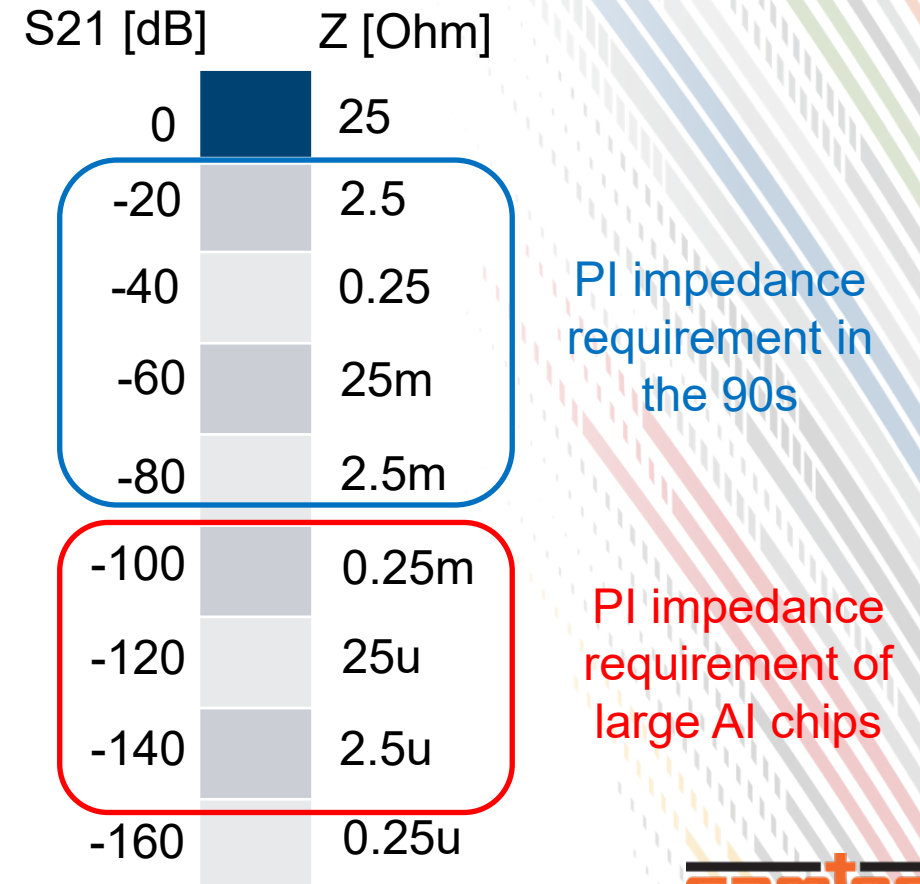
SIGNAL INTEGRITY VS. POWER INTEGRITY IN THE AI AGE

Without proper power integrity there is no signal integrity!

Xtk: SI-PI signal ratio



Self impedance



NEED IMPROVED DYNAMIC RANGE

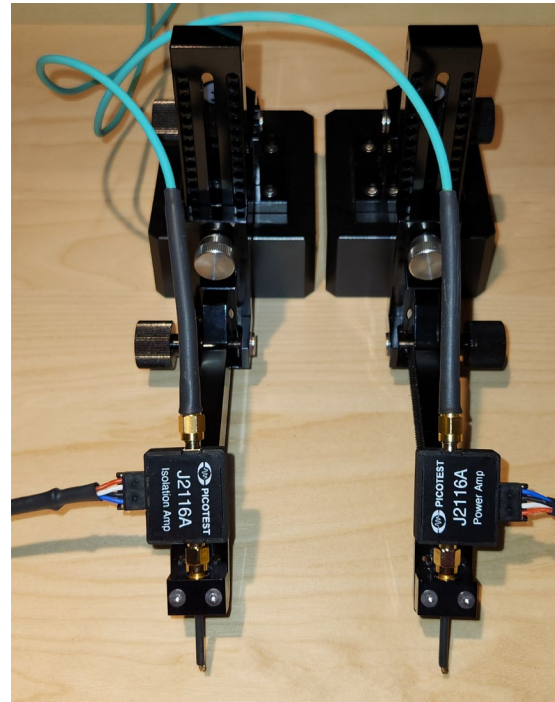
Dynamic range can be improved by (among others) any combination of

- ❖ Increased test current (power amplifier on Port1), and/or
- ❖ Low-noise preamplifier (on Port2)

Picotest battery-powered low-noise 20dB preamplifier J2116A Isolation amp



Active devices attached directly to wafer probes

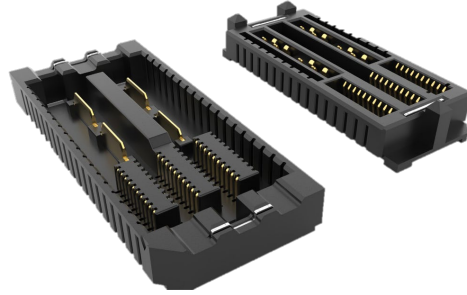


Picotest 20dB-gain +20dBm power booster J2116A Power amp



POWER-TO-SIGNAL CROSSTALK IN UDX CONNECTOR

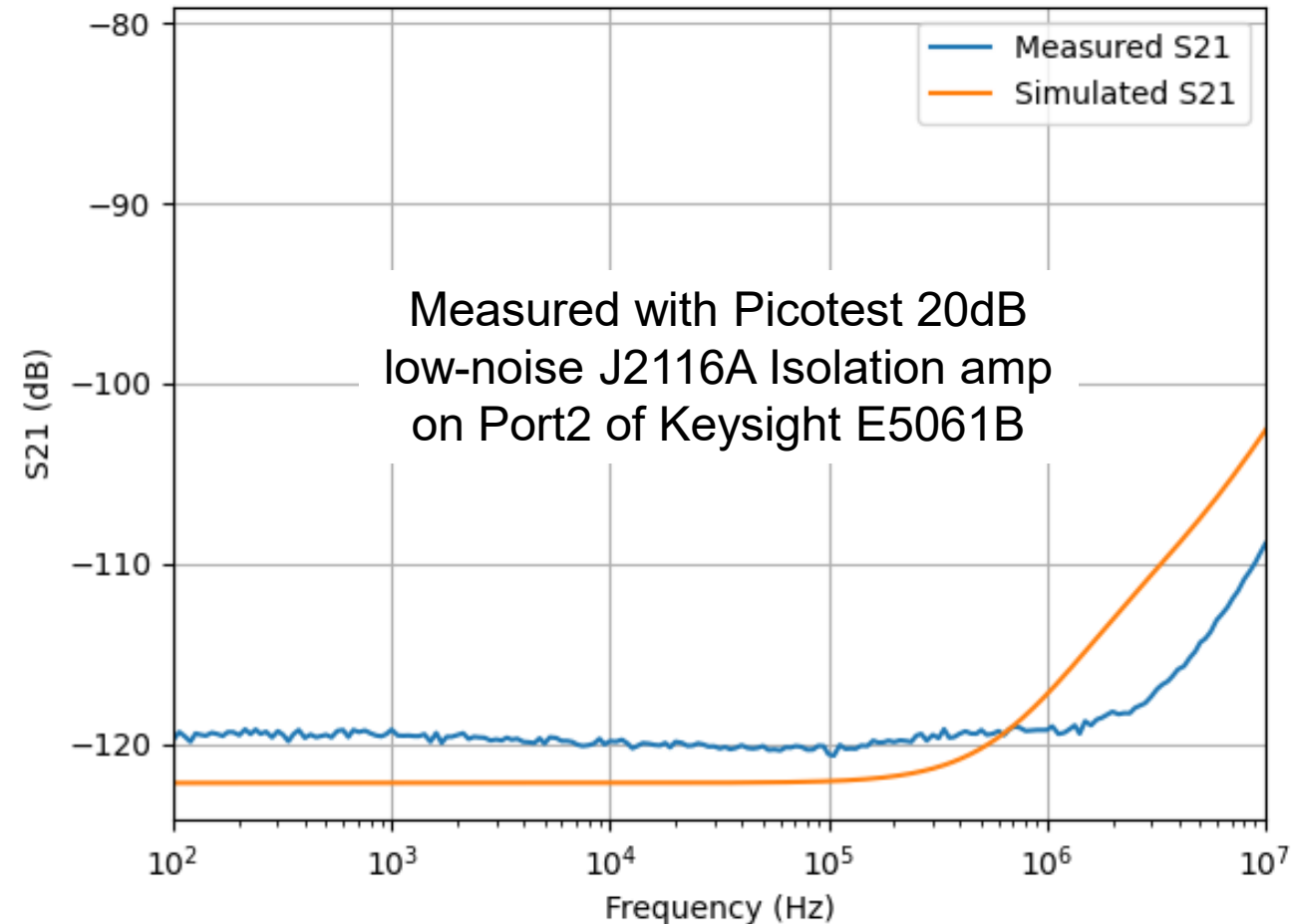
Samtec UDx6 male
and female
connectors with
four power blades
and 60 signal pins.



PI evaluation board for UDx6 connectors

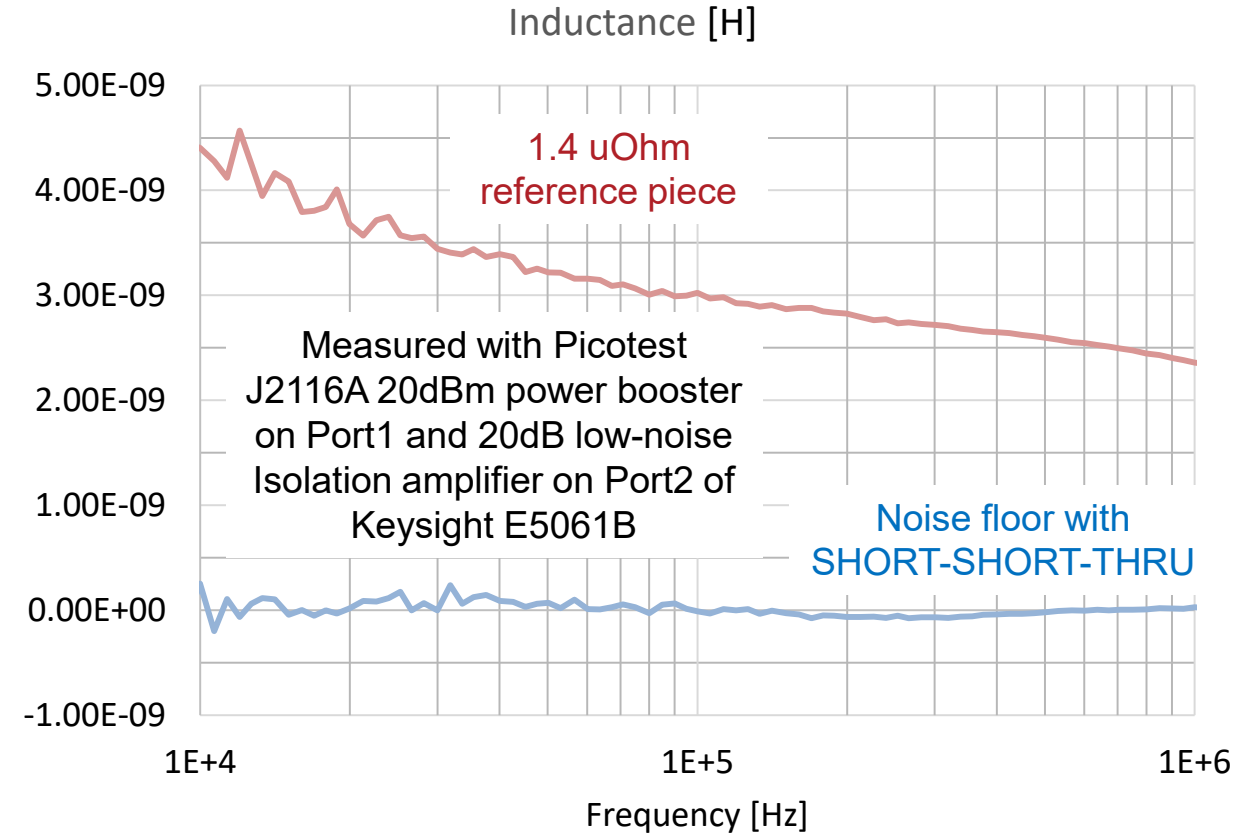
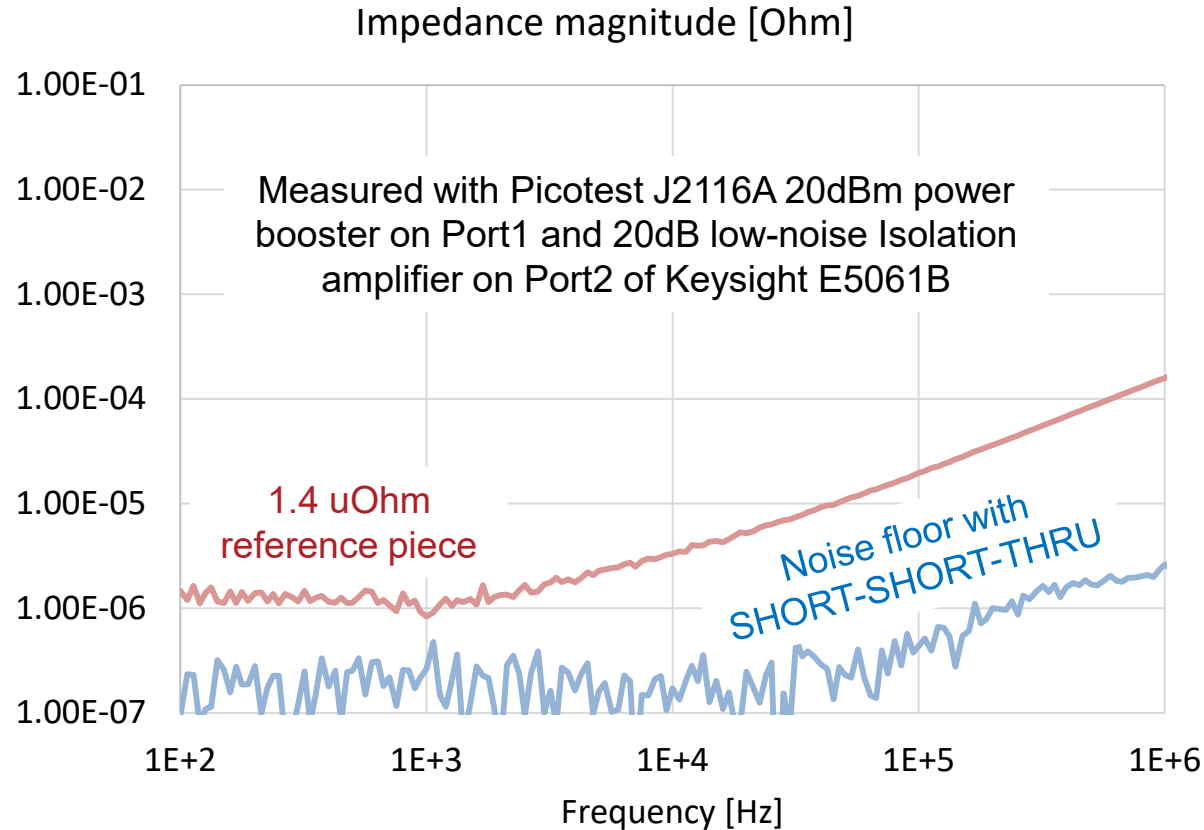


Measured and simulated power-to-signal crosstalk [dB]

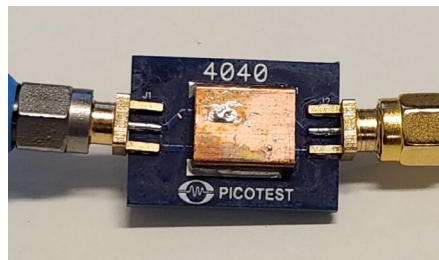


Measured with Picotest 20dB
low-noise J2116A Isolation amp
on Port2 of Keysight E5061B

IMPEDANCE MEASUREMENT NOISE FLOOR



Picotest
1.4uOhm
reference



Home-made
SHORT-
SHORT-
THRU





THANK YOU

